

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Ricardo Gonzales et al.
APPLICATION NO.: 10/815,453
FILING DATE: March 31, 2004
TITLE: Configuring a Multi-Processor System
EXAMINER: Marshall McLeod
ART UNIT: 2157
ATTY.DKT. NO.: PA2718US
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**INFORMATION DISCLOSURE STATEMENT
Under 37 C.F.R. § 1.56, and 1.97-1.98
& Statement Under 37 C.F.R. § 1.97(e)(1)**

SIR:

Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08, listing several references. The Examiner is requested to make these references of official record in the application.

The references cited may be material to examination of the application and are submitted in compliance with Applicants' duty of disclosure as defined by 37 C.F.R. § 1.56. No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Applicant has enclosed the applicable fee of \$180.00 with this submission, in accordance to C.F.R. § 1.17(p). If more fees are due, however, the Commissioner is hereby authorized to charge any necessary fees to Account Number 06-0600. A copy of this statement is submitted in duplicate for this purpose.

Respectfully submitted,

Ricardo E. Gonzalez et al.

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